

## **ABSTRACT OF THE DISCLOSURE**

An assisted generating system and method for layout data conversions converts an output file generated by a circuit design program (such as ConceptHDL). In addition to rule setting, the method automatically adjusts fields with abnormal properties. It provides  
5 a user interface with hints for the user to edit the contents. The system finally outputs a converted file compatible with a circuit layout program (such as Allegro) to assist subsequent layout designs.